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Date Mailed. 02/08/2001	Docket Due Date:	Client: Maxim Integrated Products, Inc.	J1036 U.S. PTO
Title Dual Interio	eaved DC to DC Switching C	ircuits Realized in an Integrated Circuit	09/781120
Inventor(s) Schafffer			02/08/01
The following has been re	ceived in the U.S. Patent &	Trademark Office on the date stamped hereon:	,
□ Appeal Brief & two copes Rule 1.171 Application: Research (1 pages w/ cor S □ Assignment & Cover S □ Declaration & POA □ Disclosure Docs & Inv/ ☑ Drawings: 4 shoot ☑ Express Mail No. □ 44 □ Fee Transmitted (1.184 or App. Offer Sumender	After Final (pgs) lies (pgs each) lissue ver & abstract) heet (pgs) (3pgs) By Assignee s Signed Ltr s, _5figuros 66330343US	Reply Brief (pgs) Response to Notice of Missing F Supplemental Amendment (Transmittal Letter Check No. 1 11541	(pgs)gs) r 35 U.S.C. 122(b)(2)(B)(l) Parts & PTO-1533 (pgs)